

FCA16N60 / FCA16N60_F109

600V N-Channel MOSFET

Features

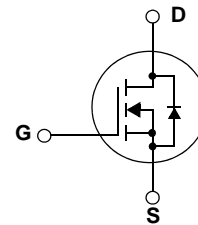
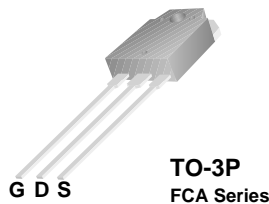
- 650V @T_J = 150°C
- Typ. R_{ds(on)}=0.22Ω
- Ultra low gate charge (typ. Q_g=55nC)
- Low effective output capacitance (typ. C_{oss,eff}=110pF)
- 100% avalanche tested
- RoHS Compliant



Description

SuperFET™ is, Fairchild's proprietary, new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance.

This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. Consequently, SuperFET is very suitable for various AC/DC power conversion in switching mode operation for system miniaturization and higher efficiency.



Absolute Maximum Ratings

Symbol	Parameter	FCA16N60	Unit
V _{DSS}	Drain-Source Voltage	600	V
I _D	Drain Current - Continuous (T _C = 25°C) - Continuous (T _C = 100°C)	16 10.1	A A
I _{DM}	Drain Current - Pulsed (Note 1)	48	A
V _{GSS}	Gate-Source voltage	± 30	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	450	mJ
I _{AR}	Avalanche Current (Note 1)	16	A
E _{AR}	Repetitive Avalanche Energy (Note 1)	16.7	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	4.5	V/ns
P _D	Power Dissipation (T _C = 25°C) - Derate above 25°C	167 1.33	W W/°C
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C
T _L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300	°C

*Drain current limited by maximum junction temperature

Thermal Characteristics

Symbol	Parameter	FCA16N60	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case	0.75	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	41.7	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FCA16N60	FCA16N60	TO-3P	-	-	30
FCA16N60	FCA16N60_F109	TO-3PN	-	-	30

Electrical Characteristics T_C = 25°C unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA, T _J = 25°C	600	--	--	V
		V _{GS} = 0V, I _D = 250μA, T _J = 150°C	--	650	--	V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C	--	0.6	--	V/°C
BV _{DS}	Drain-Source Avalanche Breakdown Voltage	V _{GS} = 0V, I _D = 16A	--	700	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 600V, V _{GS} = 0V	--	--	1	μA
		V _{DS} = 480V, T _C = 125°C	--	--	10	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30V, V _{DS} = 0V	--	--	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30V, V _{DS} = 0V	--	--	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	3.0	--	5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10V, I _D = 8A	--	0.22	0.26	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40V, I _D = 8A (Note 4)	--	11.5	--	S
Dynamic Characteristics						
C _{iSS}	Input Capacitance	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	--	1730	2250	pF
C _{oSS}	Output Capacitance		--	960	1150	pF
C _{rSS}	Reverse Transfer Capacitance		--	85	--	pF
C _{oSS}	Output Capacitance	V _{DS} = 480V, V _{GS} = 0V, f = 1.0MHz	--	45	60	pF
C _{oSS eff.}	Effective Output Capacitance	V _{DS} = 0V to 400V, V _{GS} = 0V	--	110	--	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 300V, I _D = 16A R _G = 25Ω (Note 4, 5)	--	42	85	ns
t _r	Turn-On Rise Time		--	130	270	ns
t _{d(off)}	Turn-Off Delay Time		--	165	340	ns
t _f	Turn-Off Fall Time		--	90	190	ns
Q _g	Total Gate Charge	V _{DS} = 480V, I _D = 16A V _{GS} = 10V (Note 4, 5)	--	55	70	nC
Q _{gs}	Gate-Source Charge		--	10.5	13	nC
Q _{gd}	Gate-Drain Charge		--	28	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	16	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	48	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0V, I _S = 16A	--	--	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _S = 16A di _F /dt = 100A/μs (Note 4)	--	435	--	ns
Q _{rr}	Reverse Recovery Charge		--	7.0	--	μC

NOTES:

- Repetitive Rating: Pulse width limited by maximum junction temperature
- I_{AS} = 8A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C
- I_{SD} ≤ 16A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C
- Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 2%
- Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

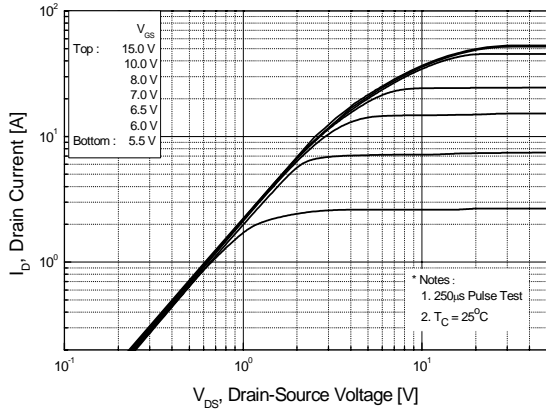


Figure 2. Transfer Characteristics

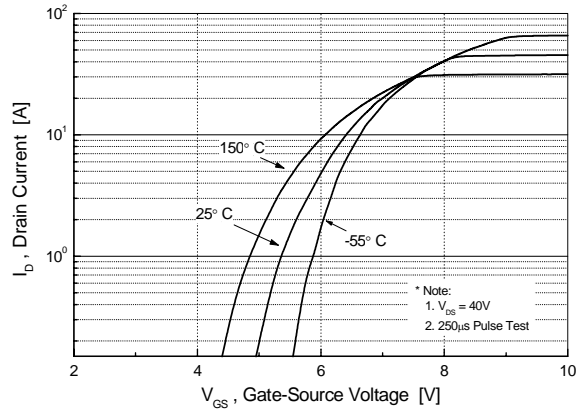


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

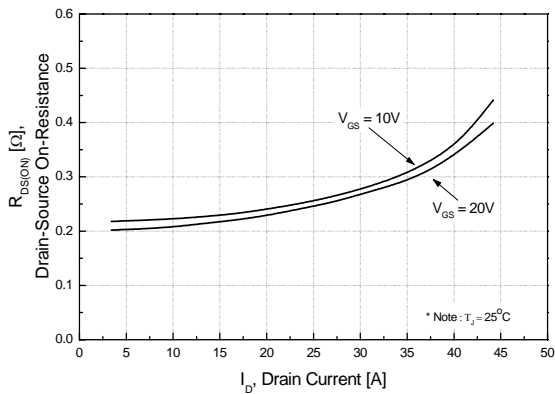


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

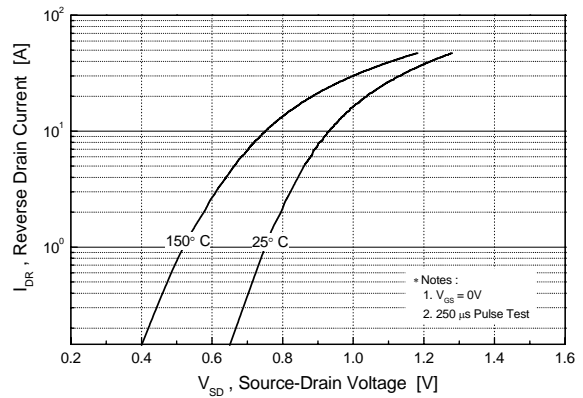


Figure 5. Capacitance Characteristics

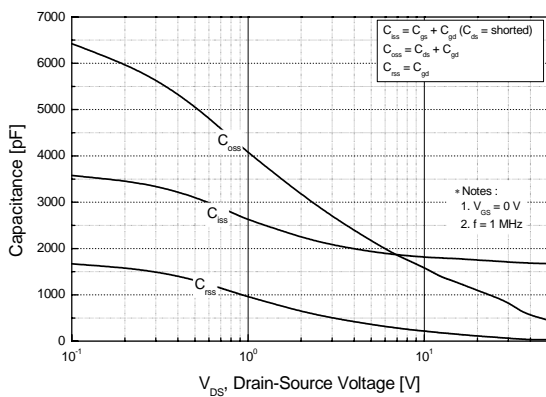
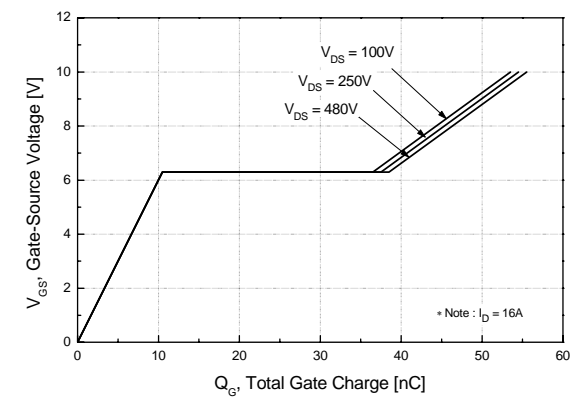


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

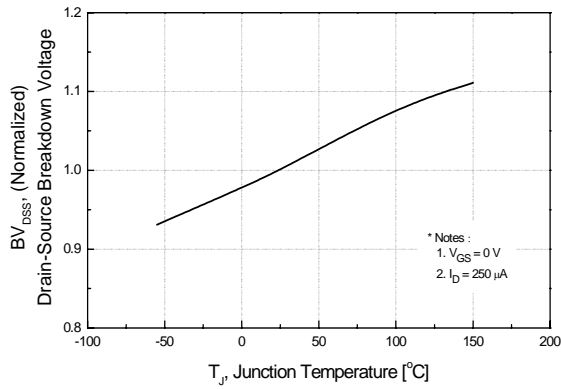


Figure 8. On-Resistance Variation vs. Temperature

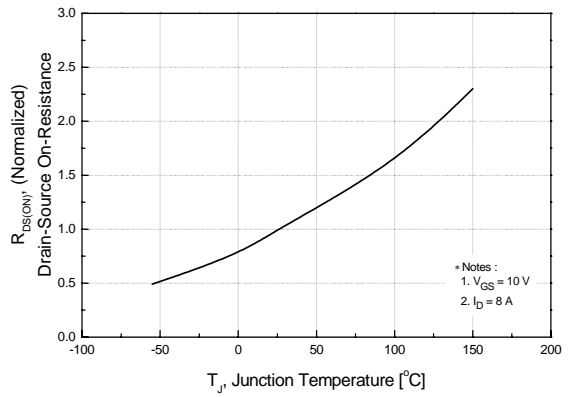


Figure 9. Maximum Safe Operating Area

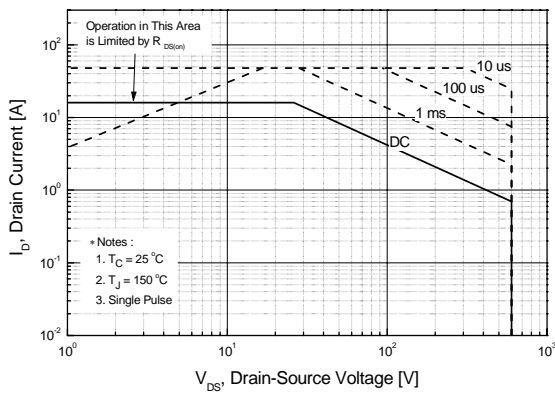


Figure 10. Maximum Drain Current vs. Case Temperature

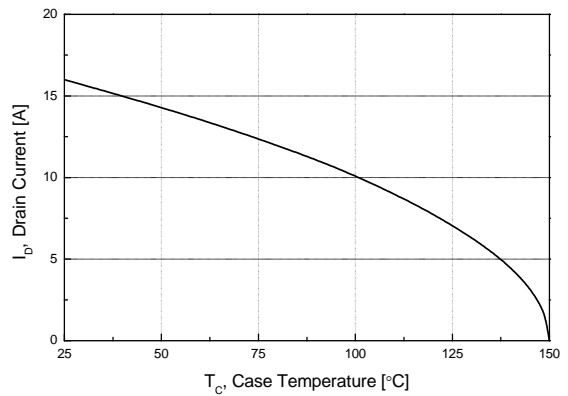
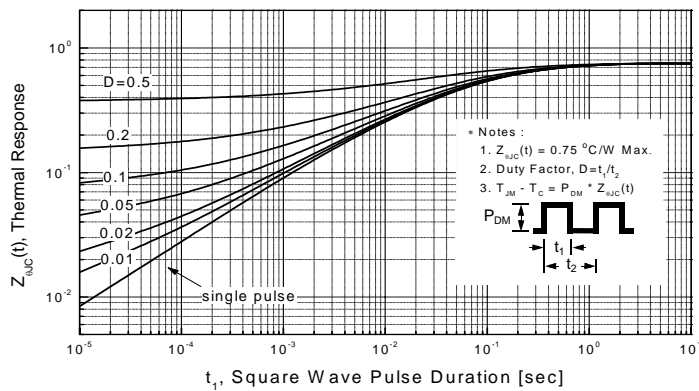
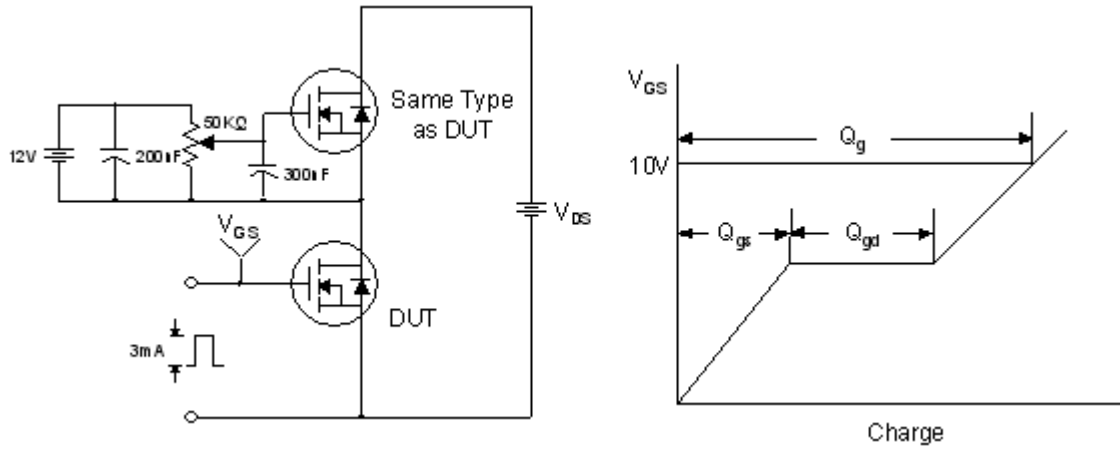


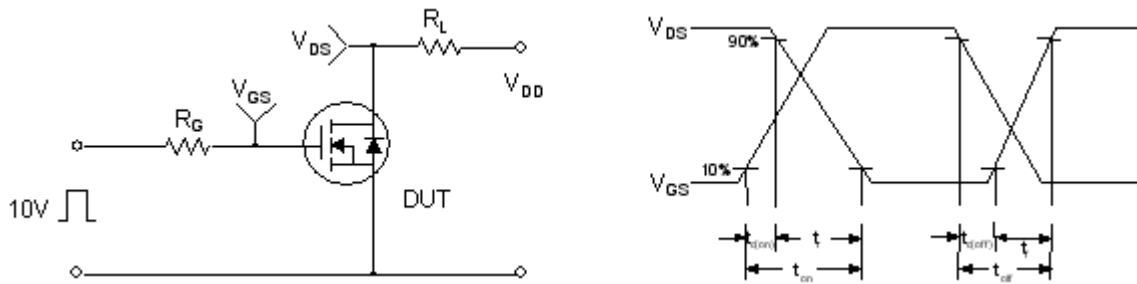
Figure 11. Transient Thermal Response Curve



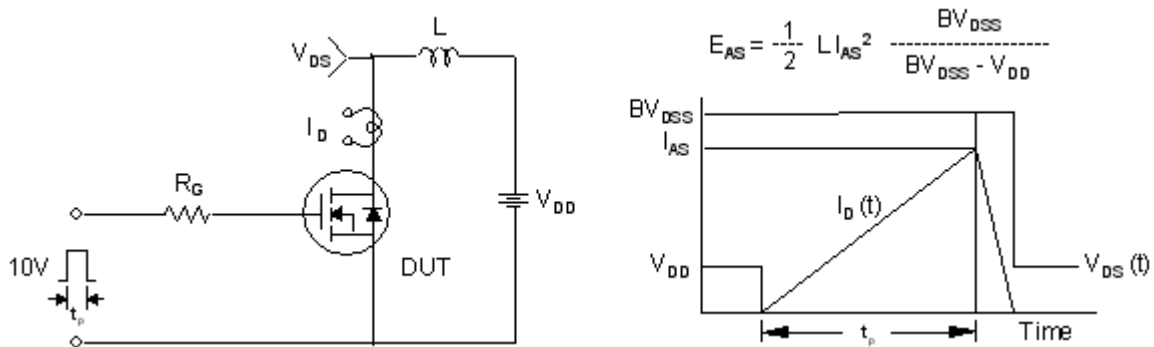
Gate Charge Test Circuit & Waveform



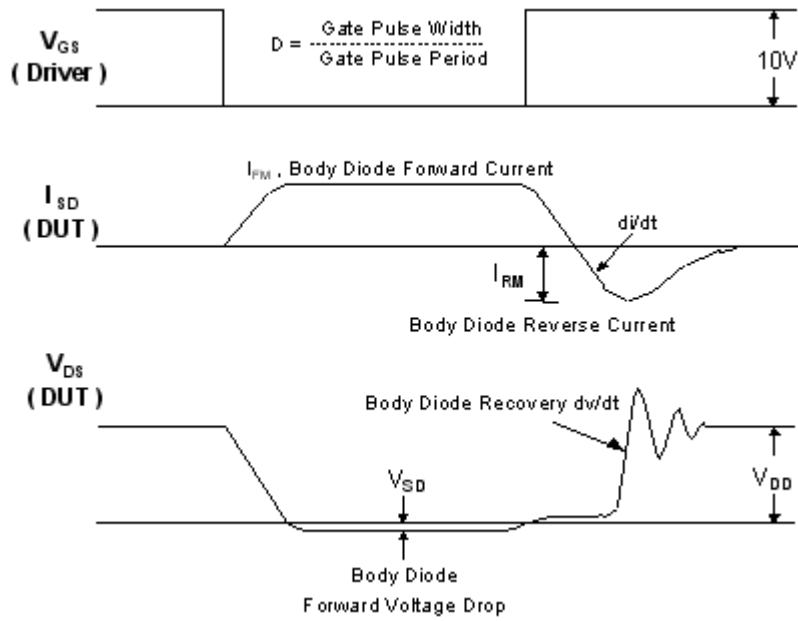
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

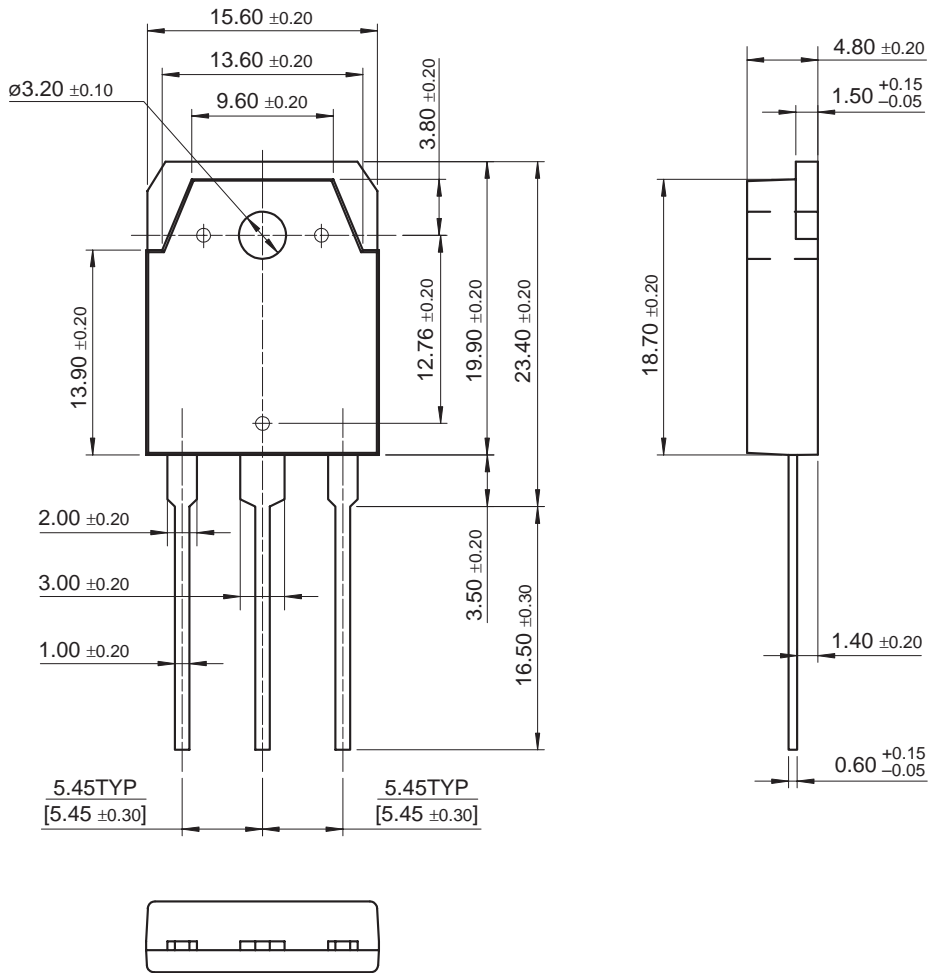


Peak Diode Recovery dv/dt Test Circuit & Waveforms



Mechanical Dimensions

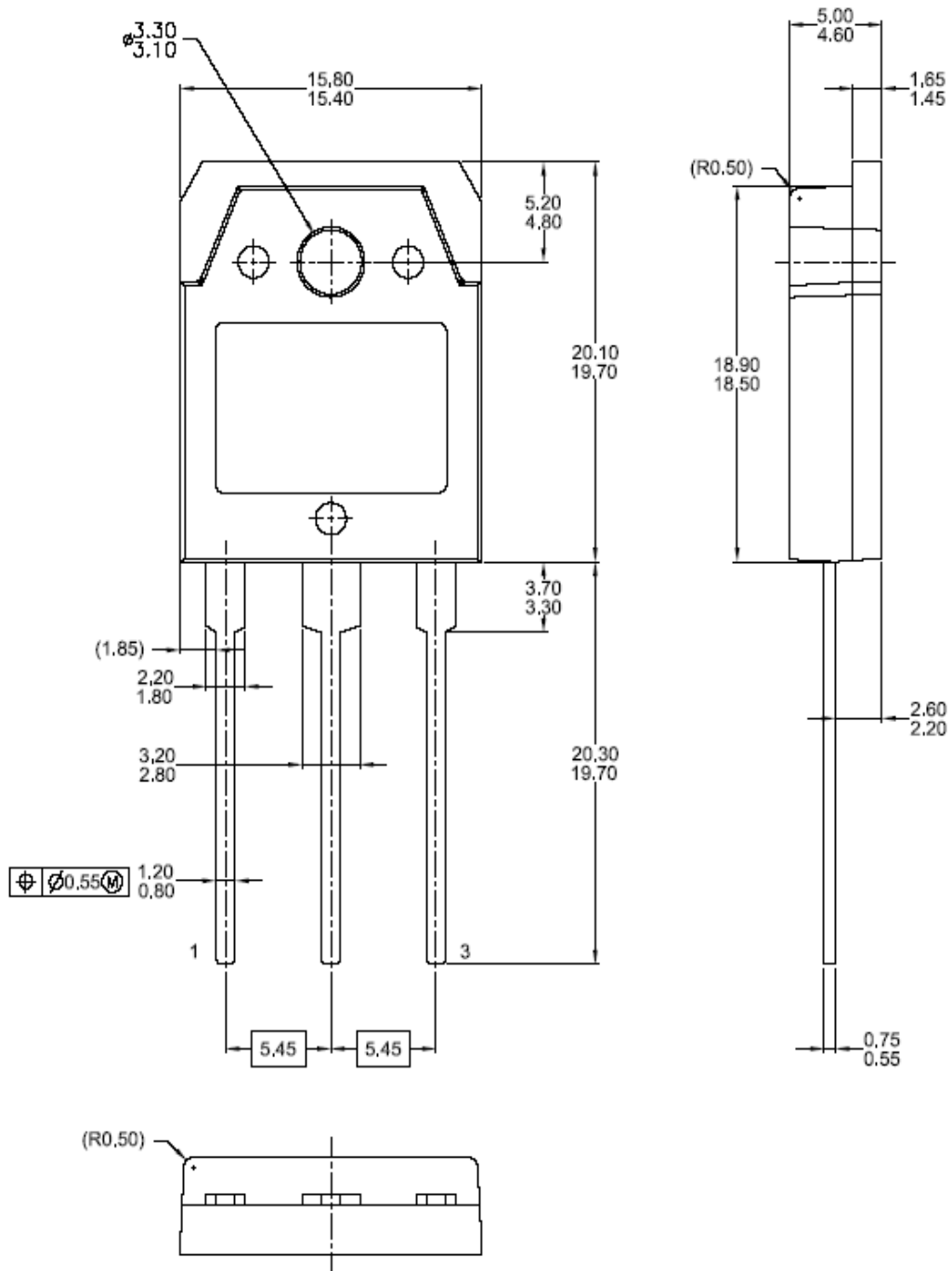
TO-3P



Dimensions in Millimeters

Mechanical Dimensions (continued)






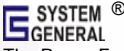
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Dimensions in Millimeters

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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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