

PCN Number:	20190715001.0	PCN Date:	Aug. 7, 2019
Title:	Datasheet for TMS320F28335, TMS320F28334, TMS320F28333, TMS320F28332 TMS320F28235, TMS320F28234, TMS320F28232		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



TMS320F28335, TMS320F28334, TMS320F28333, TMS320F28332
TMS320F28235, TMS320F28234, TMS320F28232

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Changes from October 18, 2016 to April 22, 2019 (from N Revision (October 2016) to O Revision)	Page
• Section 1.1 (Features): Updated "Package Options" feature.	1
• Section 1.2 (Applications): Updated section.	2
• Section 1.3 (Description): Updated section.	3
• Section 3.1 (Related Products): Updated section.	9
• Table 4-1 (Signal Descriptions): Updated DESCRIPTION of XRS.	20
• Table 5-4 (Clocking and Nomenclature (150-MHz Devices)): Updated footnote about smallest valid "Low-speed peripheral clock prescaler register" value.	42
• Section 5.9.2.1 (Power Management and Supervisory Circuit Solutions): Updated section.	43
• Section 5.9.4.5.1 (Master Mode Timing): Updated section.	61
• Table 5-31 (SPI Master Mode External Timing (Clock Phase = 0)): Updated MIN value (for both BRR EVEN and BRR ODD) for Parameter 23, $t_{d(SPC)M}$	61
• Table 5-32 (SPI Master Mode External Timing (Clock Phase = 1)): Updated MIN value (for both BRR EVEN and BRR ODD) for Parameter 23, $t_{d(SPC)M}$	63
• Section 5.9.4.5.2 (Slave Mode Timing): Updated section.	64
• Section 5.9.6.2 (Synchronous Mode (USEREADY = 1, READYMODE = 0)): Updated "XTIMING register configuration restrictions" table by changing XRDACTIVE value from "≥ 1" to "≥ 2" and XWRACTIVE value from "≥ 1" to "≥ 2".	75
• Section 5.9.6.2 (Synchronous Mode (USEREADY = 1, READYMODE = 0)): Updated "Examples of valid and invalid timing" table by changing Valid XRDACTIVE value from "1" to "2" and Valid XWRACTIVE value from "1" to "2".	75
• Section 5.9.6.3 (Asynchronous Mode (USEREADY = 1, READYMODE = 1)): Updated "Examples of valid and invalid timing" table (X2TIMING = 1 row) by changing Valid XRDACTIVE value from "1" to "2" and Valid XWRACTIVE value from "1" to "2".	76
• Table 5-61 (Flash Parameters at 150-MHz SYSCLKOUT): Added MAX Program Time values and MAX Erase Time values. Added footnote about program time. Added footnote about maximum flash parameter.	92
• Section 6.2.7 (Analog-to-Digital Converter (ADC) Module): Updated equations by which the digital value of the input analog voltage is derived.	122
• Table 6-32 (PIE Peripheral Interrupts): Added footnote about ADCINT.	167
• Section 8.1 (Getting Started): Updated section.	178
• Section 8.3 (Tools and Software): Updated section.	180
• Section 8.4 (Documentation Support): Updated section.	182
• Section 9.1 (Packaging Information): Added link to Packaging information website.	186

The datasheet number will be changing.

Device Family	Change From:	Change To:
TMS320F28x	SPRS439N	SPRS439O

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/TMS320F28335>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

TMS320F28232PGFA	TMS320F28234ZJZA	TMS320F28235ZJZS	TMS320F28334ZJZS
TMS320F28232PTPQ	TMS320F28234ZJZQ	TMS320F28332PGFA	TMS320F28335PGFA
TMS320F28232PTPS	TMS320F28234ZJZS	TMS320F28332PTPS	TMS320F28335PTPQ
TMS320F28232ZHHA	TMS320F28235PGFA	TMS320F28333PGFA	TMS320F28335PTPS
TMS320F28232ZJZA	TMS320F28235PTPQ	TMS320F28334PGFA	TMS320F28335ZHHA
TMS320F28234PGFA	TMS320F28235PTPS	TMS320F28334PTPS	TMS320F28335ZJZA
TMS320F28234PTPQ	TMS320F28235ZJZA	TMS320F28334ZHHA	TMS320F28335ZJZQ
TMS320F28234PTPS	TMS320F28235ZJZQ	TMS320F28334ZJZA	TMS320F28335ZJZQR
TMS320F28234ZHHA	TMS320F28235ZJZQR	TMS320F28334ZJZQ	TMS320F28335ZJZS

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