

Product Overview

NLV17SZ07: Single Non-Inverting Buffer, Open Drain

For complete documentation, see the data sheet.

The NLV17SZ07 is a high performance single non-inverting buffer, operating from 1.65-5.5 V, available in SC70/SC88a/SOT-353 package.

Features

- Extremely High Speed: t_{PD} 2.5 ns (typical) at $V_{CC} = 5$ V
- Designed for 1.65 V to 5.5 V V_{CC} Operation, CMOS compatible
- Over Voltage Tolerant Inputs, V_{in} may be between 0 and 7V for V_{CC} between 0.5 and 5.5V
- TTL Compatible - Interface Capability with 5 V TTL Logic with $V_{CC} = 3$ V
- 24 mA Output Sink Capability, pull up may be between 0 and 7V
- Near Zero Static Supply Current Substantially Reduces System Power Requirements
- Chip Complexity: FET = 20
- Tiny SOT-353 package
- Pb-Free Packages are Available

Part Electrical Specifications

Product	Compliance	Status	Channels	Output	V_{CC} Min (V)	V_{CC} Max (V)	t_{pd} Max (ns)	I_o Max (mA)	Package Type
NLV17SZ07DFT2G	AEC Qualified PPAP Capable Pb-free Halide free	Active	1	Open Drain	1.65	5.5	3.5	24	SC-88A / SC-70-5

For more information please contact your local sales support at www.onsemi.com.

Created on: 8/26/2019