

CIRRUS LOGIC Process Change Notification

PCN Number: PCN-2016-65

PCN Notification Date: 10/11/2016

FINAL PCN

WM8993ECS/R Assembly and Test Transfer to Amkor Korea

Dear Customer,

This notification is to advise you of the following change(s).

WM8993 assembly and test is being transferred from Unisem Malaysia to Amkor Korea.

If you have any questions, please contact your Sales Representative.

Sincerely,

Quality Systems Administrator Cirrus Logic Corporate Quality Phone: +1(512) 851-4000



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Products Affected:

The devices listed on this page are the complete list of affected devices. According to our records, these are the devices that you have purchased within the past twenty-four (24) months. The corresponding customer part number is also listed, if available.

Technical details of this Process / Product Change follow on the next page(s).

Title: WM8993ECS/R a		assembly and test transfer to Amkor Korea								
Customer Contact: Local Field Sales		s Representative		Phone: (512) 851-40		000	Dept:	Corp	orate Quality	
Proposed 1 st Ship Date:		Esti 201	mated Q4 6	Estimated Sample Availability Date: Now			Now			
Change Type:										
Х	Assembly Site		Х	Assembly Process			Х	Assembly Materials		
	Wafer Fab Site			Wafer Fab Process				Wafer Fab Materials		aterials
	Wafer Bump Site			Wafer Bump Process			х	Wafer Bump Material		Material
Х	Test Site			Test Process				Design		
Electrical Specification			Mechanical Specification		۱		Part Nu	mber		
x Packing/Shipping/Labeling		Х	Other							
Con	Comments: POD change (he		eight)							

PCN Details

Description of Change:

- Assembly and test will move from Unisem Malaysia to Amkor Korea
- MSL level will remain the same: MSL 1
- Package quantity in Reel will remain the same : 3.5k
- Marking will change, and logo will be removed, details below
- Minor changes in Package Outline Diagram (POD)
 - These will have no significant impact on footprint or height.
 - The details can be found in PODs below.
- Key Changes for new Site:

	CURRENT	NEW
Assembly Location	Unisem Malaysia	Amkor K4
Test Location	Unisem Malaysia	Amkor K3
Package Size	3.640 x 3.540 x 0.7 (refer to POD below)	3.550 x 3.650 x 0.546 (refer to POD below)
Solder Alloy	SAC305	SAC405
Package Outline Diagram	Package thickness : 0.7mm (POD below)	Package thickness : 0.546mm (POD below)



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Package	Outline Di	iagram									
<u>Unisem P</u>	OD										
2. D 3. E	1. Package Bottom View 2. Die size with guard ring but not saw street= 141.73mils x 137.80mils (3600um x 3500um) 3. Estimated die size with saw street = 144.88mils x 140.94mils (3680um x 3580um) 4. Pitch = 0.500mm										
*DEVIC	E: V	VM8993		*ASS	SEMBLY SITE:	Unis	em	7			
*PACK/	*PACKAGE TYPE: 48 Ball W-CSP										
*REVISI	ON: WM899	3-CSPRD	L-EV1-REV	2.01							
					Y, 0.50 mm BALL P		DM060.C				
					⊣						
Symbols			ons (mm)	NOTE	4						
Α	0.615	NOM 0.7	MAX 0.785	NOTE	-						
A1	0.219	0.244	0.269		1						
A2	0.361	0.386	0.411]						
D	3.60	3.64	3.68		4						
D1	0.55	3.00 BSC	0.50		4						
E	3.50	3.54	3.58		4						
E1 e		3.00 BSC 0.50 BSC		5	-						
f1	0.3 BSC	0.00 850		0	1						
f2	0.25 BSC				1						
9	0.035	0.070	0.105		1						
h		0.314 BSC]						

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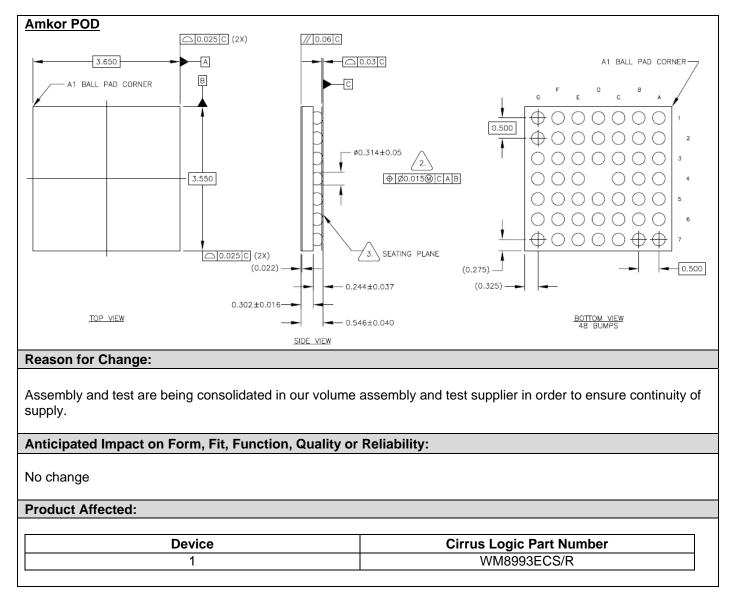
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Ch	anges To Product Identification Resulting F	From This PCN:	
	CURRENT MARKING	NEW MARKING	
	WM8993E YMTTGRC	WM8993E YMTT7YH	

Qualification Data:

This qualification has been specifically dev qualification data validates that the propose specifications.			-
Qualification Complete	Aug 2016	Status:	PASSED – Qual report is available



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		-, I	Reliability Report	Repo Date App	6	08	oject ID 2063 -July-2016 ussell McMilla	
Purp	ose				Statu	s		
Qualif	ication of WM8993ECS			Qualification successful				
WM8	93ECS							
Fab:	GlobalFoundries, Fab 5	Assembly:	Unisem (M) Berhad		Rev:	н	Package:	48 Ball W- CSP
Stress	s Name	Method	Conditions		L	.ot	Read Poi	nt Results (Fail/Sampl
HTOL	(High Temperature Op Life)	JESD22-A108	3 125°C / Dynamic Bias VDDmax	s <i>1</i>		1	1000 Hou	rs 0/77
ESD H	IBM (Human Body Model)	JESD22-A114	25°C			1	2000 Vol	ts 0/3
ESD C	CDM (Charged Device Model)	JESD22-C101	25°C			1	500 Vol	ts 0/3
Latch-	Up Over Voltage (VDD)	JESD78	85°C			1	1.5xVDDma	ax 0/3
Latch-	Up Current Injection (I/O)	JESD78	85°C			1	+/- 100 m	nA 0/3

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EIRRUS LOGIC			Reliability Report: Project ID 2	063-0		Date: 08-	July-2016
WM8	993ECS						
Fab: GlobalFoundries, Fab 5 Assembly:		Assembly:	Amkor K4, Korea Re		н	Package:	48 Ball W- CSP
Stres	s Name	Method	Conditions		Lot	Read Poi	nt Results (Fail/Sample
Preco	ndition	JESD22-A113	3 24HR 125°C Bake 168HR 85°C/85%RH Soak 3 pass 260°C peak reflow	1	1	Precon MSI	L1 0 / 154
THB (Temp	perature Humidity Bias)	JESD22-A101	85°C / 85%RH / VDDmax Post Precondition		1	1000 Hou	ırs 0/77
Temp	erature Cycle	JESD22-A104	-65 °C / +150 °C / air to air Post Precondition		1	500 Cycle	es 0/77
HTSL (High	Temperature Storage Life)	JESD22-A103	3 150°C		1	1000 Hou	rs 0 / 77

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