

Features

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotiveproducts/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

- Package: PowerDI[®]123
- Package Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (€3)
- Weight: 0.01 grams (Approximate)

PowerDI123



Ordering Information (Note 4)

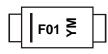
| Part Number | Bookago | Packing | | |
|---------------|------------|---------|-------------|--|
| Fait Nulliber | Package | Qty. | Carrier | |
| DFLS130-7 | PowerDI123 | 3000 | Tape & Reel | |

EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



F01 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: J = 2022)

M = Month (ex: 9 = September)



Date Code Key

Notes:

| Year | 2004 | | 2022 | 2023 | 2024 | 2025 | 2026 | 2027 | 2028 | 2029 | 2030 | 2031 |
|-------|------|-----|------|------|------|------|------|------|------|------|------|------|
| Code | R | | J | K | L | М | Ν | 0 | Р | R | S | Т |
| | | | | | | | | | | | | |
| Month | Jan | Feb | Mar | Apr | Мау | Jun | Jul | Aug | Sep | Oct | Nov | Dec |

PowerDI is a registered trademark of Diodes Incorporated.



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

| Characteristic | Symbol | Value | Unit |
|---|---------------------|-------|------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | Vrrm Vrwm Vr | 30 | V |
| RMS Reverse Voltage | V _{R(RMS)} | 21 | V |
| Average Forward Current | I _{F(AV)} | 1.0 | А |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load | IFSM | 35 | А |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|---|------------------|-------------|------|
| Power Dissipation (Note 5) | PD | 1.67 | W |
| Power Dissipation (Note 6) | PD | 556 | mW |
| Typical Thermal Resistance Junction to Ambient (Note 5) | Reja | 60 | °C/W |
| Typical Thermal Resistance Junction to Ambient (Note 6) | Reja | 180 | °C/W |
| Typical Thermal Resistance Junction to Soldering (Note 7) | Rejs | 10 | °C/W |
| Operating Temperature Range | TJ | -55 to +125 | °C |
| Storage Temperature Range | T _{STG} | -55 to +150 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

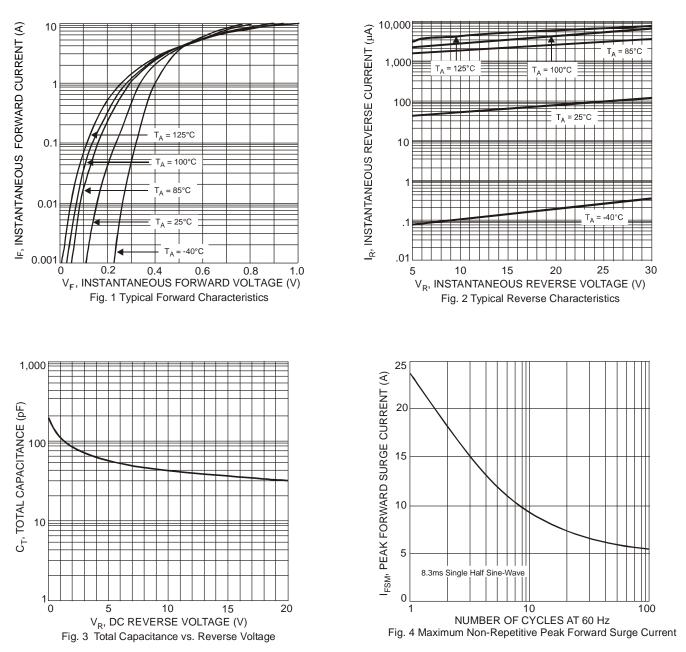
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition |
|------------------------------------|--------------------|-----------|----------------------|------------------|------|--|
| Reverse Breakdown Voltage (Note 8) | V _{(BR)R} | 30 | — | — | V | I _R = 1.5mA |
| Forward Voltage (Note 8) | VF | | 0.25 0.33 0.36 | 0.37 0.42 | V | IF = 0.1A IF = 0.7A IF = 1.0A |
| Leakage Current (Note 8) | IR | — | 0.15 | 1.0 | mA | V _R = 30V, T _A = +25°C |
| Total Capacitance | Ст | _ | 40 | _ | pF | V _R = 10V, f = 1.0MHz |

Notes:

Part mounted on 50.8mm x 50.8mm GETEK board with 25.4mm x 25.4mm copper pad, 25% anode, 75% cathode.
Part mounted on FR-4 board with 1.8mm x 2.5mm cathode and 1.8mm x 1.2mm anode, 1 oz. copper pads.
Theoretical Reus calculated from the top center of the die straight down to the PCB cathode tab solder junction.

8. Short duration pulse test used to minimize self-heating effect.



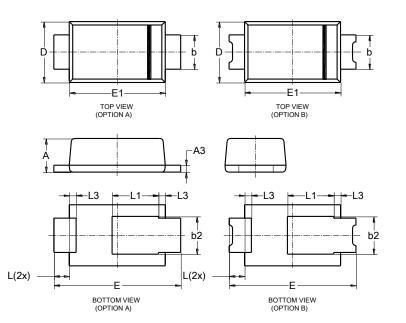




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



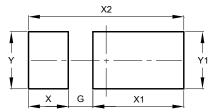


| (r | | | | | | |
|------------|----------------------|-------|------|--|--|--|
| PowerDI123 | | | | | | |
| Dim | Min | Мах | Тур | | | |
| Α | 0.93 | 1.00 | 0.98 | | | |
| A3 | 0.15 | 0.25 | 0.20 | | | |
| b | 0.85 | 1.25 | 1.00 | | | |
| b2 | 1.025 | 1.125 | 1.10 | | | |
| D | 1.63 | 1.93 | 1.78 | | | |
| E | 3.50 | 3.90 | 3.70 | | | |
| E1 | 2.60 | 3.00 | 2.80 | | | |
| L | 0.40 | 0.50 | 0.45 | | | |
| L1 | 1.25 | 1.40 | 1.35 | | | |
| L3 | 0.125 | 0.275 | 0.20 | | | |
| All | All Dimensions in mm | | | | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123



| Dimensions | Value | | |
|------------|---------|--|--|
| Dimensions | (in mm) | | |
| G | 0.65 | | |
| Х | 1.05 | | |
| X1 | 2.40 | | |
| X2 | 4.10 | | |
| Y | 1.50 | | |
| Y1 | 1.50 | | |



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