MICROCHIP

Product Change Notification / SYST-14YIDP728

Date:

17-Mar-2014

Product Category:

16-Bit - Microcontrollers and Digital Signal Controllers

PCN Type:

Document Change

Notification Subject:

Data Sheet - PIC24FJ128GA310 Data Sheet Data Sheet Document Revision

Affected CPNs:

SYST-14YIDP728_Affected_CPN_03172014.pdf
SYST-14YIDP728_Affected_CPN_03172014.csv

Notification Text:

SYST-14YIDP728

Microchip has released a new DeviceDoc for the PIC24FJ128GA310 Data Sheet of devices. If you are using one of these devices please read the document located at PIC24FJ128GA310 Data Sheet.

Notification Status: Final

Description of Change:
1) Updated Table 1-4 (CTED1 through CTED13 are digital, not analog). Removed references to Timer1 clock.
2) Removed the Low-Power SOSC Operation section from **Section 9.0 "Oscillator Configuration"**.
3) Added note to **Section 9.5.3 "SOSC Layout Considerations"**.
4) Added BOREN1 bit to Register 29-2.
5) Updated description of ALTVRF<1:0> in Register 29-2.
6) Minor text edits throughout the document.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 15 Mar 2014

NOTE: Please be advised that this is a change to the document only the product has not been changed..

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

PIC24FJ128GA310 Data Sheet

Please contact your local Microchip sales office with questions or concerns regarding this notification.

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