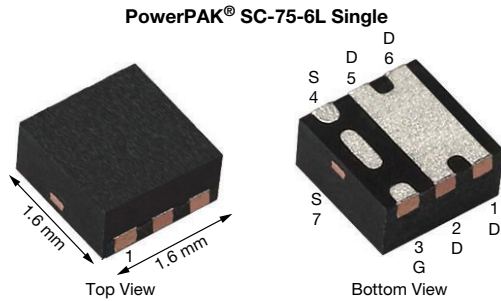


N-Channel 20 V (D-S) MOSFET



Marking code: AF

PRODUCT SUMMARY	
V_{DS} (V)	20
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 4.5$ V	0.030
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 2.5$ V	0.041
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 1.8$ V	0.057
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 1.5$ V	0.082
Q_g typ. (nC)	6
I_D (A) ^a	9
Configuration	Single

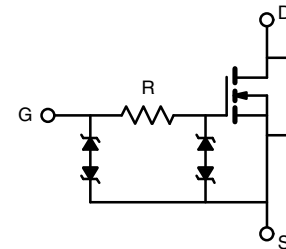
FEATURES

- TrenchFET[®] power MOSFET
- Thermally enhanced PowerPAK[®] SC-75 package
 - Small footprint area
 - Low on-resistance
 - Thin 0.75 mm profile
- Typical ESD protection 4000 V
- 100 % R_g tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
 COMPLIANT
 HALOGEN
FREE

APPLICATIONS

- Portable devices
 - Load switch
 - Battery switch



ORDERING INFORMATION	
Package	PowerPAK SC-75
Lead (Pb)-free and halogen-free	SiB422EDK-T1-GE3

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-source voltage	V_{DS}	20	V			
Gate-source voltage	V_{GS}	± 8				
Continuous drain current ($T_J = 150$ °C)	I_D	$T_C = 25$ °C	9 ^a			
		$T_C = 70$ °C	9 ^a			
		$T_A = 25$ °C	7.1 ^{b, c}			
		$T_A = 70$ °C	5.7 ^{b, c}			
Pulsed drain current	I_{DM}	25	A			
Continuous source-drain diode current	I_S	$T_C = 25$ °C			9 ^a	
		$T_A = 25$ °C			2.1 ^{b, c}	
Maximum power dissipation	P_D	$T_C = 25$ °C			13	
		$T_C = 70$ °C	8.4			
		$T_A = 25$ °C	2.5 ^{b, c}			
		$T_A = 70$ °C	1.6 ^{b, c}			
Operating junction and storage temperature range	T_J, T_{stg}	-55 to +150	°C			
Soldering recommendations (peak temperature) ^{d, e}		260				

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient ^{b, f}	R_{thJA}	41	51	°C/W
Maximum junction-to-case (drain)	R_{thJC}	7.5	9.5	

Notes

- Package limited, $T_C = 25$ °C
- Surface mounted on 1" x 1" FR4 board
- $t = 5$ s
- See solder profile (www.vishay.com/doc?73257). The PowerPAK SC-75 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection
- Rework conditions: manual soldering with a soldering iron is not recommended for leadless components
- Maximum under steady state conditions is 105 °C/W



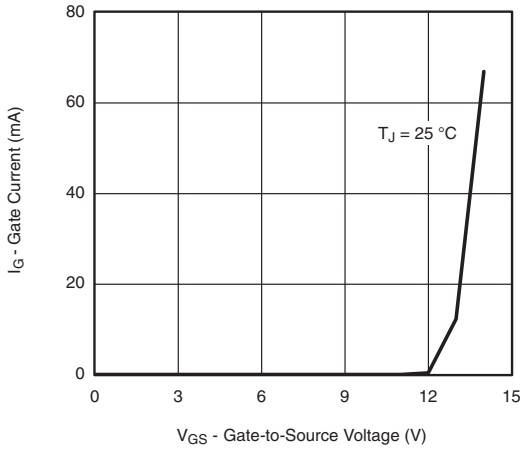
SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	20	-	-	V
V _{DS} temperature coefficient	ΔV _{DS} /T _J	I _D = 250 μA	-	18	-	mV/°C
V _{GS(th)} temperature coefficient	ΔV _{GS(th)} /T _J		-	-2.5	-	
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	0.4	-	1.0	V
Gate-source leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 4.5 V	-	-	± 1.5	μA
		V _{DS} = 0 V, V _{GS} = ± 8 V	-	-	± 25	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V	-	-	1	
		V _{DS} = 20 V, V _{GS} = 0 V, T _J = 55 °C	-	-	10	
On-state drain current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 4.5 V	15	-	-	A
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 5 A	-	0.025	0.030	Ω
		V _{GS} = 2.5 V, I _D = 4.3 A	-	0.034	0.041	
		V _{GS} = 1.8 V, I _D = 1.5 A	-	0.046	0.057	
		V _{GS} = 1.5 V, I _D = 1 A	-	0.055	0.082	
Forward transconductance ^a	g _{fs}	V _{DS} = 10 V, I _D = 5 A	-	28	-	S
Dynamic ^b						
Total gate charge	Q _g	V _{DS} = 10 V, V _{GS} = 8 V, I _D = 7.1 A	-	11.5	18	nC
			-	6	9	
			-	0.8	-	
Gate-source charge	Q _{gs}	V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 7.1 A	-	1.6	-	nC
Gate-drain charge	Q _{gd}		-	1.6	-	
Gate resistance	R _g	f = 1 MHz	460	2300	4600	Ω
Turn-on delay time	t _{d(on)}	V _{DD} = 10 V, R _L = 1.8 Ω I _D ≅ 5.7 A, V _{GEN} = 4.5 V, R _g = 1 Ω	-	0.3	0.45	μs
Rise time	t _r		-	0.6	0.9	
Turn-off delay time	t _{d(off)}		-	3.8	6	
Fall time	t _f		-	1.7	2.6	
Turn-on delay time	t _{d(on)}	V _{DD} = 10 V, R _L = 1.8 Ω I _D ≅ 5.7 A, V _{GEN} = 10 V, R _g = 1 Ω	-	0.15	0.25	
Rise time	t _r		-	0.3	0.45	
Turn-off delay time	t _{d(off)}		-	5.6	9	
Fall time	t _f		-	1.6	2.5	
Drain-Source Body Diode Characteristics						
Continuous source-drain diode current	I _S	T _C = 25 °C	-	-	9	A
Pulse diode forward current	I _{SM}		-	-	25	
Body diode voltage	V _{SD}	I _S = 5.7 A, V _{GS} = 0 V	-	0.85	1.2	V
Body diode reverse recovery time	t _{rr}	I _F = 5.7 A, di/dt = 100 A/μs, T _J = 25 °C	-	15	30	ns
Body diode reverse recovery charge	Q _{rr}		-	7.5	15	nC
Reverse recovery fall time	t _a		-	8	-	ns
Reverse recovery rise time	t _b		-	15	-	

Notes

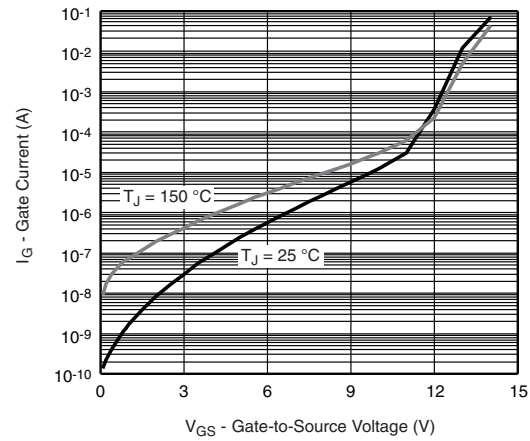
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

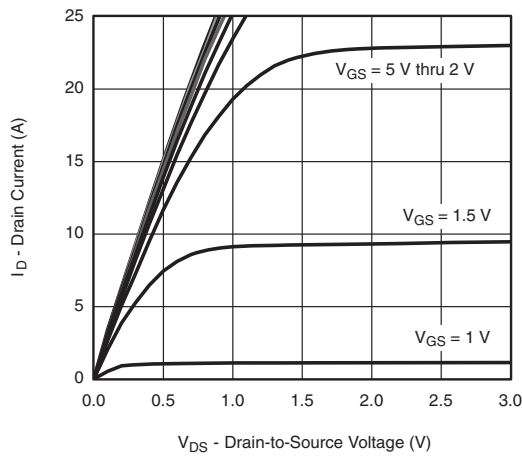
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



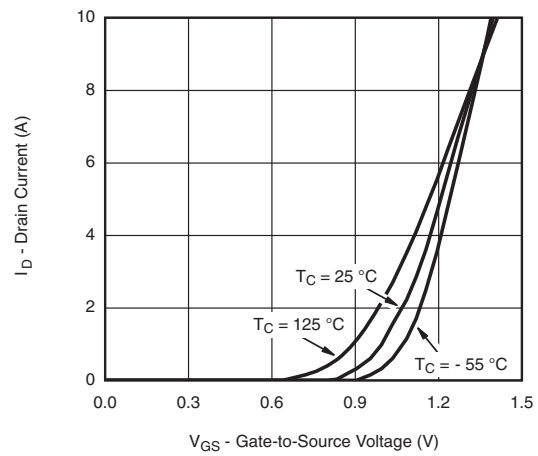
Gate Current vs. Gate-to-Source Voltage



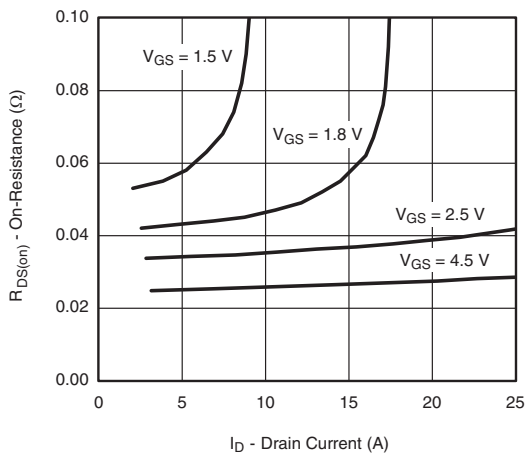
Gate Current vs. Gate-to-Source Voltage



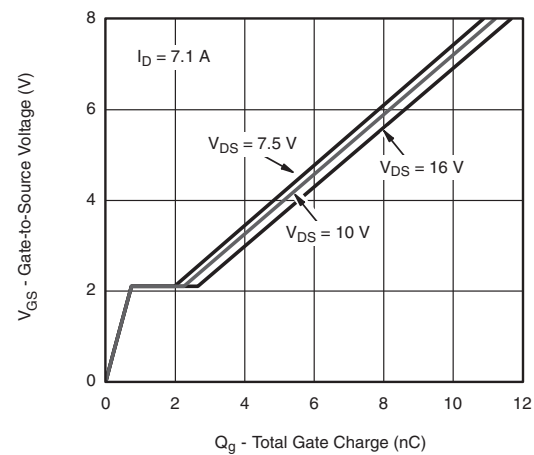
Output Characteristics



Transfer Characteristics



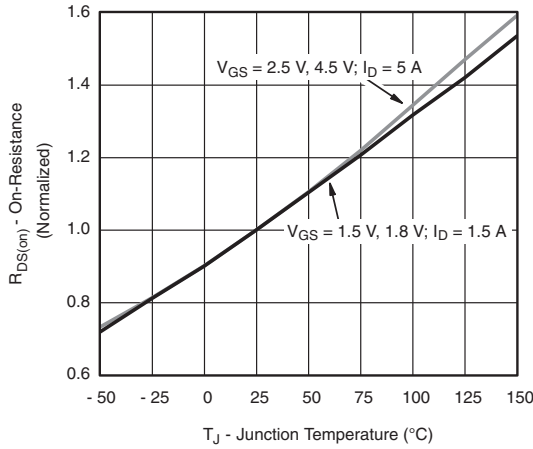
On-Resistance vs. Drain Current



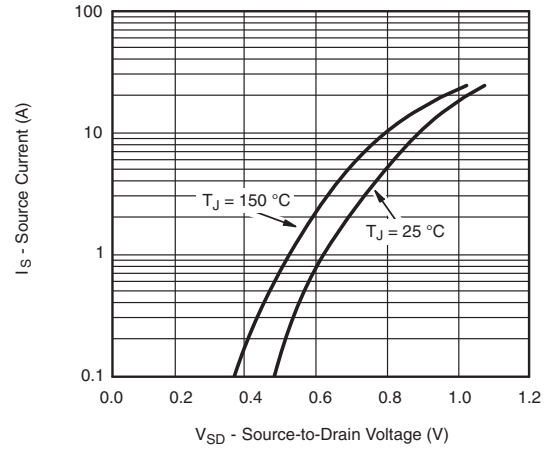
Gate Charge



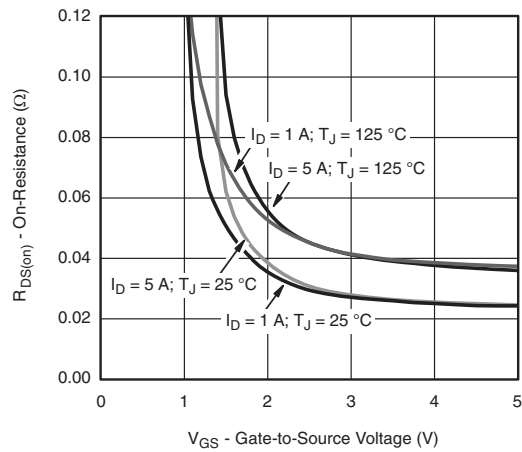
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



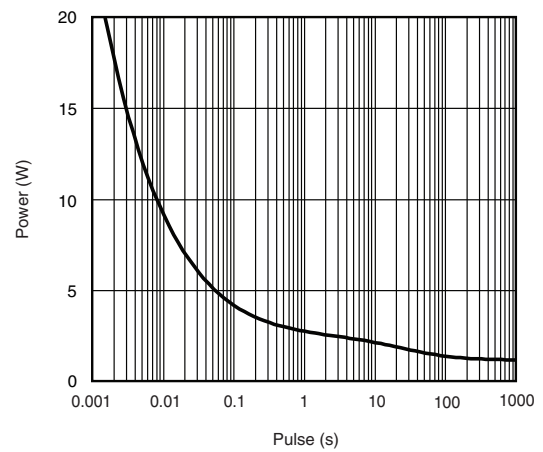
Normalized On-Resistance vs. Junction Temperature



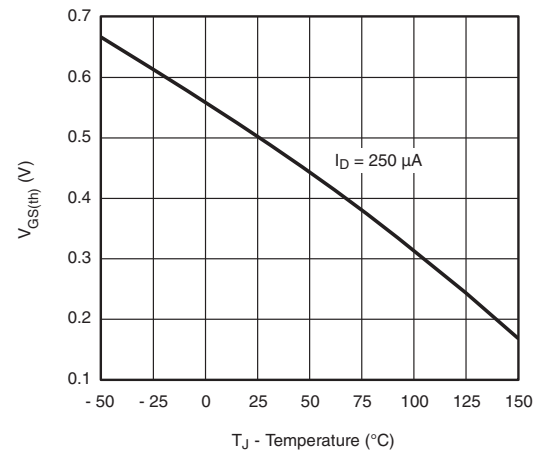
Source-Drain Diode Forward Voltage



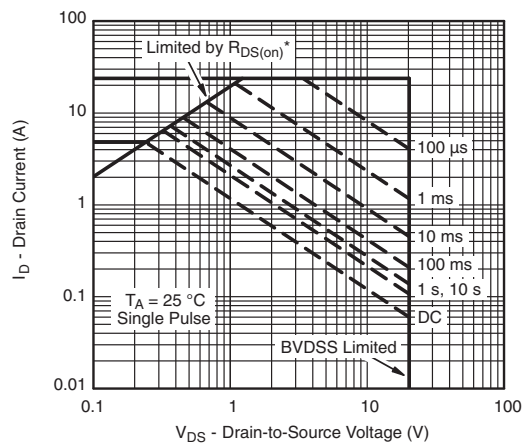
On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient



Threshold Voltage

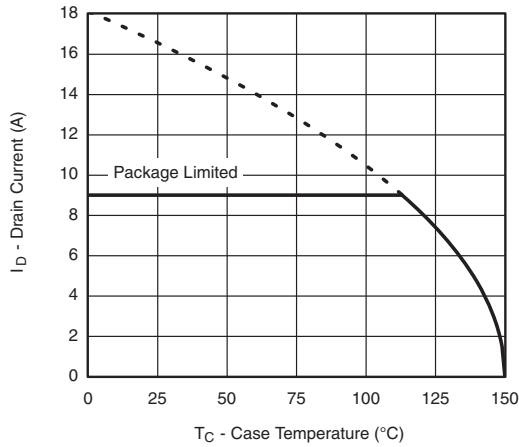


* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

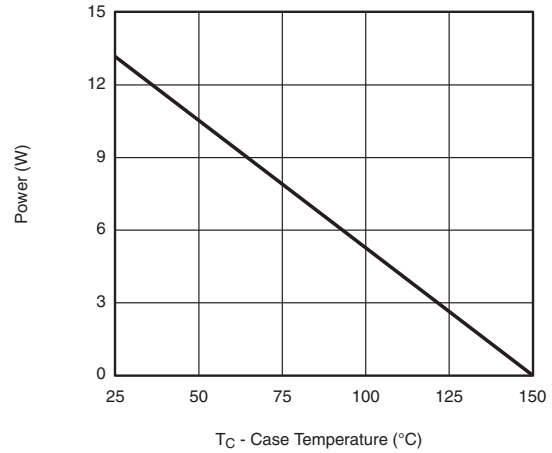
Safe Operating Area, Junction-to-Ambient



TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



Current Derating ^a



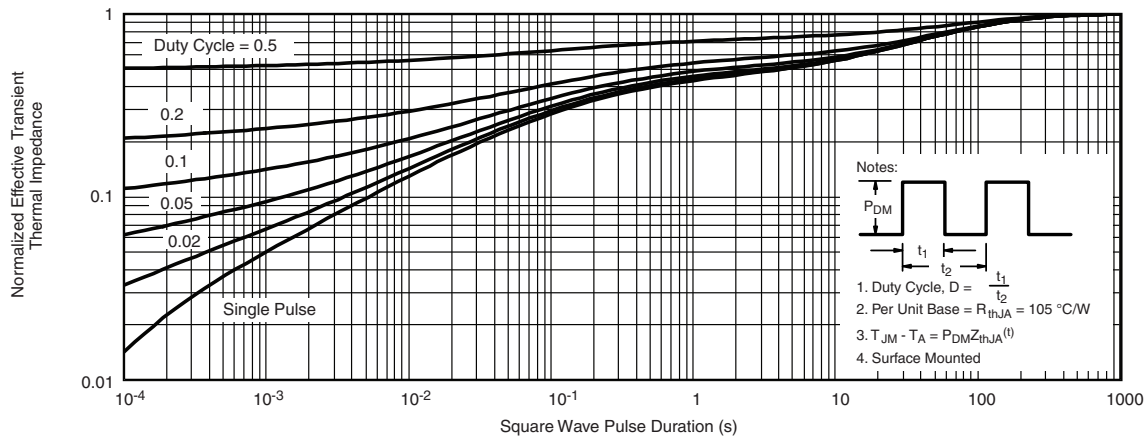
Power Derating

Note

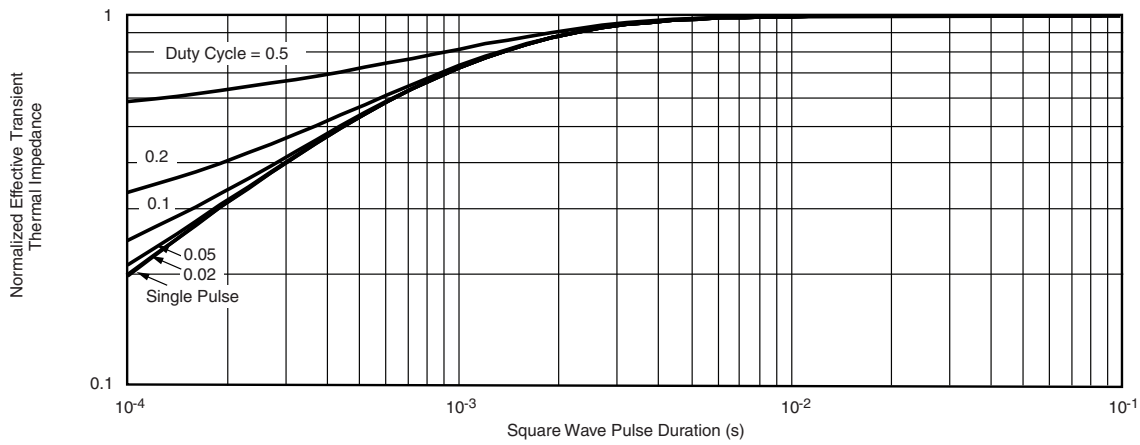
- a. The power dissipation P_D is based on T_J max. = $150\text{ }^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?65297.



PowerPAK® SC75-6L



BACKSIDE VIEW OF SINGLE

BACKSIDE VIEW OF DUAL



Notes:

1. All dimensions are in millimeters
2. Package outline exclusive of mold flash and metal burr
3. Package outline inclusive of plating

DETAIL Z

DIM	SINGLE PAD						DUAL PAD					
	MILLIMETERS			INCHES			MILLIMETERS			INCHES		
	Min	Nom	Max	Min	Nom	Max	Min	Nom	Max	Min	Nom	Max
A	0.675	0.75	0.80	0.027	0.030	0.032	0.675	0.75	0.80	0.027	0.030	0.032
A1	0	-	0.05	0	-	0.002	0	-	0.05	0	-	0.002
b	0.18	0.25	0.33	0.007	0.010	0.013	0.18	0.25	0.33	0.007	0.010	0.013
C	0.15	0.20	0.25	0.006	0.008	0.010	0.15	0.20	0.25	0.006	0.008	0.010
D	1.53	1.60	1.70	0.060	0.063	0.067	1.53	1.60	1.70	0.060	0.063	0.067
D1	0.57	0.67	0.77	0.022	0.026	0.030	0.34	0.44	0.54	0.013	0.017	0.021
D2	0.10	0.20	0.30	0.004	0.008	0.012						
E	1.53	1.60	1.70	0.060	0.063	0.067	1.53	1.60	1.70	0.060	0.063	0.067
E1	1.00	1.10	1.20	0.039	0.043	0.047	0.51	0.61	0.71	0.020	0.024	0.028
E2	0.20	0.25	0.30	0.008	0.010	0.012						
E3	0.32	0.37	0.42	0.013	0.015	0.017						
e	0.50 BSC			0.020 BSC			0.50 BSC			0.020 BSC		
K	0.180 TYP			0.007 TYP			0.245 TYP			0.010 TYP		
K1	0.275 TYP			0.011 TYP			0.320 TYP			0.013 TYP		
K2	0.200 TYP			0.008 TYP			0.200 BSC			0.008 TYP		
K3	0.255 TYP			0.010 TYP								
K4	0.300 TYP			0.012 TYP								
L	0.15	0.25	0.35	0.006	0.010	0.014	0.15	0.25	0.35	0.006	0.010	0.014
T							0.03	0.08	0.13	0.001	0.003	0.005

ECN: C-07431 – Rev. C, 06-Aug-07
DWG: 5935

RECOMMENDED PAD LAYOUT FOR PowerPAK® SC75-6L Single



[Return to Index](#)



Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.