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REVISION HISTORY

12/2017—Rev. A to Rev. B

Document Title Changed from CN0026 to AN-1489..... Universal
Changes to Figure 1 1

5/2009—Rev. 0 to Rev. A

Updated Format..... Universal

10/2008—Revision 0: Initial Version

CIRCUIT DESCRIPTION

The [AD5547/AD5557](#) are dual-channel, precision 16-bit/14-bit, multiplying, low power, current output, parallel input DACs. They operate from a single 2.7 V to 5.5 V supply with ± 15 V multiplying references for four-quadrant outputs. Built in four-quadrant resistors facilitate the resistance matching and temperature tracking that minimize the number of components needed for multi-quadrant applications.

This circuit uses the [ADR03](#), which is a high accuracy, high stability, 2.5 V precision voltage reference. Because voltage reference temperature coefficient and long-term drift are primary considerations for applications requiring high precision conversion, the [ADR03](#) is an ideal candidate.

An op amp is used in the current to voltage stage of this circuit. The bias current and offset voltage of the op amp are both important selection criteria for use with precision current output DACs. Therefore, this circuit employs the [AD8628](#) auto-zero op amp, which has ultralow offset voltage (1 μ V typical) and bias current (30 pA typical). The compensation capacitor, C7, is optimized to compensate for the external output capacitance of the DAC.

Note that the [AD8628](#) has rail-to-rail (R-2R) input and output stages, but the output can only come within a few millivolts of either rail depending on the load current. For the circuit shown in Figure 1, the output can swing from -2.5 V to approximately -1 mV.

The input offset voltage of the op amp is multiplied by the variable noise gain (due to the code dependent output resistance of the DAC) of the circuit. A change in this noise gain between two adjacent digital codes produces a step change in the output voltage due to the input offset voltage of the amplifier. This output voltage change is superimposed on the desired change in output between the two codes and gives rise to a differential linearity error, which, if large enough, can cause the DAC to be nonmonotonic. In general, the input offset voltage must be a fraction of an LSB to ensure monotonic behavior when stepping through codes. For the [ADR03](#) and the [AD5547](#), the LSB size is

$$\frac{2.5 \text{ V}}{2^{16}} = 38 \mu\text{V} \quad (1)$$

The input offset voltage of the [AD8628](#) auto-zero op amp is typically 1 μ V, which is negligible compared to the LSB size.

The input bias current of an op amp also generates an offset at the voltage output as a result of the bias current flowing through the feedback resistor, RFB. In the case of the [AD8628](#), the input bias current is only 30 pA typical, which when flowing through the RFB resistor (10 k Ω typical), produces an error of only 0.3 μ V.

The [AD5547/AD5557](#) DAC architecture uses a current steering R-2R ladder design that requires an external reference and op amp to convert to an output voltage. V_{OUT} can be calculated for the [AD5547](#) using the following equation:

$$V_{OUT} = \frac{-V_{REF} \times D}{2^{16}} \quad (2)$$

where D is the decimal equivalent of the input code.

V_{OUT} can be calculated for the [AD5557](#) using the following equation:

$$V_{OUT} = \frac{-V_{REF} \times D}{2^{14}} \quad (3)$$

COMMON VARIATIONS

For multichannel applications, the [AD8629](#) is a dual version of the [AD8628](#). The [AD8605](#) is another excellent op amp candidate for the current to voltage conversion circuit. It also has a low offset voltage and low bias current. The [ADR01](#) and [ADR02](#) are other low noise references available from the same reference family as the [ADR03](#). Other suitable low noise references are the [ADR441](#) and [ADR445](#) devices. The size of the reference input voltage is restricted by the R-2R voltage of the op amp selected.

The circuit shown in Figure 1 can also be used as a variable gain element by utilizing the multiplying bandwidth nature of the R-2R structure of the [AD5547/AD5557](#) DAC. In this configuration, remove the external precision reference and apply the signal to be multiplied to the reference input pins of the DAC.

REFERENCES

[ADIsimPower Design Tool.](#)

[Kester, Walt. 2005. Chapter 3 and Chapter 7 in *The Data Conversion Handbook*. Analog Devices.](#)

[MT-015 Tutorial, *Basic DAC Architectures II: Binary DACs*. Analog Devices.](#)

[MT-031 Tutorial, *Grounding Data Converters and Solving the Mystery of AGND and DGND*. Analog Devices.](#)

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